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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : James Schleicher et al.
Application No. : 10/797,484 Conf. No.: 3295
Filed : March 9, 2004
For : INTERCONNECTION RESOURCES FOR
PROGRAMMABLE LOGIC INTEGRATED
CIRCUIT DEVICES
Group Art Unit : 2819
Examiner : Vibol Tan

Mail Stop Issue Fee
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT PURSUANT
TO 37 C.F.R. § 1.312

Sir:

Please amend the above-identified allowed patent
application (for which the issue fee has not yet been paid)
as follows:

Amendments to the specification begin on page 2.

Remarks begin on page 3.

OK to
enter this
Amendment.
Thank you
LTan
3/15/05